

# Leading the Future of Logic with Materials-Enabled Scaling



## KEVIN MORAES, PH.D.

*Corporate Vice President  
Strategy and Marketing, Semiconductor Products Group*

Kevin leads Applied Materials' global strategy and marketing initiatives, driving growth across key semiconductor segments. He focuses on aligning market insights with business priorities, shaping competitive positioning, and enabling customer success through technology inflections. Prior to his current role, Kevin held leadership positions across product management and technology development at Applied. He holds a Bachelor's degree in Chemical Engineering from Annamalai University, an MBA from UC Berkeley, and a Ph.D. in Materials Science and Engineering from Rensselaer Polytechnic Institute.

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## BALA HARAN, PH.D.

*Corporate Vice President  
Integrated Materials Solutions, Semiconductor Products Group*

Dr. Bala Haran is the Corporate Vice President of the Integrated Materials Solutions Division at Applied Materials. In his role Bala leads a team of device integrators and process engineers focused on identifying and enabling new inflections in Logic and Memory technology. His current focus is on integrating Applied processes and tools to provide leading-edge performance elements for Nanosheet-based CMOS Logic and next-generation DRAM and 3D NAND devices. Prior to joining Applied Materials, Bala was at IBM for 18 years where he led multi-company process and device integration teams focused on CMOS technology development and transfer. He holds a Ph.D. in Chemical Engineering from the University of South Carolina and has more than 50 peer-reviewed publications and 100 patents granted.

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**ZHEBO CHEN, PH.D.**

*Senior Director  
Metal Deposition Products, Semiconductor Products Group*

Dr. Zhebo Chen serves as the Chief Marketing Officer for the Atomic Layer Deposition and Metal Deposition Products business units at Applied Materials, overseeing Global Product Marketing, Strategic Marketing, and Product Line Management. He played a key role in a wide spectrum of deposition technologies during his 10 years of tenure at Applied across numerous generations of semiconductor devices in logic, memory, and specialty applications. He holds a B.S. in Chemical Engineering from the University of Illinois at Urbana-Champaign and a Ph.D. in Chemical Engineering from Stanford University.

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## MICHAEL SULLIVAN

*Corporate Vice President  
Head of Investor Relations*

Michael Sullivan is Corporate Vice President and head of Investor Relations. His team is responsible for investor relations and marketing communications including media relations, product and technology communications, and industry events. Mr. Sullivan joined Applied in 2009 after working at Intel Corporation for 16 years. He was Intel's primary interface to equity analysts and the company's largest institutional shareholders in the U.S. and Europe. He also held corporate communications positions at the company's U.S. and European offices, where he drove corporate and competitive initiatives, new microprocessor introductions, computing platform campaigns, and flash memory PR. Mr. Sullivan is past president of the Silicon Valley chapter of NIRI, the National Investor Relations Institute. He earned his MBA at Santa Clara University and his BA in Public Relations at San Jose State University.